

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S58	3	(Mambakkam-Sreenath.in. Venkidu-Arockiyaswamy.in. Jones-Larry-Lawson.in.) and @pd>"20040823"	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/02/02 15:43
S59	384	(700/231.ccls. 361/737.ccls. 710/302.ccls. 710/301.ccls. 710/64.ccls. 710/63.ccls. 710/62.ccls. 235/492.ccls. 235/435.ccls. 235/439.ccls. 235/440.ccls. 235/441.ccls.) and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 15:47
S60	8	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 16:13
S61	2	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) and @pd>"20040823"	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 16:27
S62	0	(scsi (small adj computer adj system adj interface)) near5 (simple adj display adj device) and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 16:28
S63	10	(scsi (small adj computer adj system adj interface)) near5 (display screen lcd) and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 16:29
S64	0	(scsi (small adj computer adj system adj interface)) near5 (display screen lcd) near5 (command\$3 instruction\$1) and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 16:40
S65	0	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1)) same parallel and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 16:40

S66	1	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3) and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 16:41
S67	4	((memory smart ic chip circuit) adj1 card\$1) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3) and @pd>"20040823"	US-PGPUB; USPAT	OR	OFF	2005/02/02 16:42
S68	1	((memory smart ic chip circuit) adj1 card\$1) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3) and @pd>"20040823"	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 16:53